

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

Accurate Generation of Scan Enable Signal when Testing  
Integrated Circuits Using Sequential Scanning Techniques

Application Number :

Confirmation Number:

First Named Applicant: Hasanuzzaman SHEIKH

Attorney Docket Number: TI-36330

Art Unit:

Examiner:

Search string: ( 10302886 ).pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	10302886	2002-11-25	Gupte et al			

## Signature

Examiner Name	Date